

U.S. Department of Commerce, Patent and Trademark Office

Attorney Docket No.: 004-4896

Application No.: 09/710,218

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Applicant(s): Timothy L. Harris

(Use several sheets if necessary)

Filing Date: November 10, 2000

Group Art Unit: 2151

Date Submitted: September 18, 2001

NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
27	AA	Arora, Blumofe and Plaxton, <i>Thread Scheduling for Multiprogrammed Multiprocessors</i> , in <i>Proceedings of the 10th Annual ACM Symposium on Parallel Algorithms and Architectures</i> (1998) pp. 119-129.
27	AB	Greenwald and Cheriton, <i>The Synergy Between Non-Blocking Synchronization and Operating System Structure</i> , Dept. of Computer Science, Univ. of Rochester, NY (1998), 14 pp.
27	AC	Greenwald, <i>Non-Blocking Synchronization and System Design</i> , PhD thesis, Stanford University Technical Report STAN-CS-TR-99-1624, Palo Alto, CA, (1999) pp. 1-24.
27	AD	LaMarca, <i>A Performance Evaluation of Lock-Free Synchronization Protocols</i> in <i>Proceedings of the 13th Annual ACM Symposium on Principles of Distributed Computing</i> Los Angeles, CA, (Aug. 14-17, 1994), pp. 130-140.
27	AE	Massalin and Pu, <i>A Lock-free Multiprocessor OS Kernel</i> , Tech. Rep. TRCUCS-005-9, Columbia University, New York, NY, (1991) 21 pp.
27	AF	Michael and Scott, <i>Correction of a Memory Management Method for Lock-Free Data Structures</i> , TR599, Dept. of Computer Science, Univ. of Rochester, NY (Dec. 1995) pp. 1-7.
27	AG	Michael and Scott, <i>Non-Blocking Algorithms and Preemption-Safe Locking on Multiprogrammed Shared Memory Multiprocessors</i> , Dept. of Computer Science, Univ. of Rochester, NY (1998) pp. 1-24.
27	AH	Michael and Scott, <i>Simple, Fast, and Practical Non-Blocking and Blocking Concurrent Queue Algorithms</i> in <i>15th ACM Symposium on Principles of Distributed Computing</i> (May 1996) 9 pp.
27	AI	Valois, <i>Lock-Free Linked Lists Using Compare-and-Swap</i> , in <i>Proceedings of the Fourteenth ACM Symposium on Principles of Distributed Computing</i> , Ottawa, Canada (Aug. 1995) pp. 214-222.
	AJ	
	AK	
	AL	
	AM	
	AN	
	AO	
	AP	
	AQ	

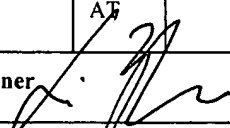
Examiner

Date Considered

9/25/2003

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

RECEIVED
SEP 24 2001
Technology Center 2100

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 004-4896			
				Application No.: 09/710,218			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Applicant(s): Timothy L. Harris			
				Filing Date: November 10, 2000			
				Group Art Unit: 2151			
				Date Submitted: February 1, 2002			
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
Foreign Patent Documents							
							Translation
		Document	Date	Country	Class	Subclass	Yes No
Lg	AL	EP 0 466 339 A2 -	15.01.92	EPO			
	AM						
	AN						
	AP						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
Lg	AR	Ole Agesen et al., "DCAS-Based Concurrent Deques," Proceedings of the Twelfth Annual ACM Symposium on Parallel Algorithms and Architectures (SPAA2000), Bar Harbor, Maine, online, 9-12 July 2000, pp. 137-146 XP002172095, Association for Computing Machinery, ACM, New York, NY, [retrieved from the Internet on 2001-07-13: URL: http://research.sun.com/jtech/subs/00-dequel.ps].					
Lg	AS	Henry Massalin, "Synthesis: An Efficient Implementation of Fundamental Operating System Services," Dissertation submitted in partial fulfillment of the requirements for the Degree of Doctor of Philosophy in the Graduate School of Arts and Sciences, Columbia University, New York, NY, online, 1992; pp. 1-149 XP002172093 [retrieved from the Internet on 2001-07-13: URL: ftp://ftp.cs.columbia.edu/reports/reports-1992/cucs-039-92.ps.gz].					
	AT						
Examiner 			Date Considered 9/25/2003				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 004-4896		
				Application No.: 09/710,218		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Timothy Harris		
(Use several sheets if necessary)				Filing Date: November 10, 2000		
				Group Art Unit: 2151		
				Date Submitted: March 26, 2002		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
Foreign Patent Documents						
					Translation	
		Document	Date	Country	Yes	No
27	AL	EP 0 366 585 A ~	May 2, 1990	EPO		
47	AM	WO 86 00434 A -	Jan. 16, 1986	PCT		
	AN					
	AP					
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
10	AR	David L. Detlefs et al., "Even Better DCAS-Based Concurrent Deques," Distributed Computing, 14th International Conference, 4 Oct. 2000, pp. 59-73, XP002172096.				
	AS					
	AT					
Examiner:			Date Considered 9/25/2003			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.						